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23494 7590 10/10/2008

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Marianna Smith	(Depositor's name)
<i>Marianna Smith</i>	(Signature)
December 17, 2008	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/785,648	02/24/2004	Timothy A. Rost	TI-36595	3709

TITLE OF INVENTION: Transistor Design and Layout for Performance Improvement with Strain

APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1,440	\$300	\$0	\$1,740	01/12/2009

EXAMINER	ART UNIT	CLASS-SUBCLASS
Pham, Long	2814	257-369000

- Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).
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- For printing on the patent front page, list
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1	Jacqueline J. Garner
2	W. James Brady III
3	Frederick J. Telecky, Jr.

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

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Please check the appropriate assignee category or categories (will not be printed on the patent) ☐ individual ☒ corporation or other private group entity ☐ government

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Authorized Signature

Jacqueline J. Garner

Date December 17, 2008

Typed or printed name: Jacqueline J. Garner

Registration No. 36,144

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